

DETAIL-F 14



SEE DETAIL-F [14]

SEE DETAIL C

SEE DETAIL A.



SECTION **E-E**
SCALE: NONE

DETAIL **12**
SCALE: NONE
JUMPER CONFIGURATIONS

LTR	ZONE	DESCRIPTION	DATE	APPROVE
		SEE SHEET 1		

1 **2** **3** **4** **7** **6** **5**

○ ○ ○ ○ ○ ○ ○

-03 THRU -06
40 COL

○ ○ ○ ○ ○ ○ ○

1 **2** **3** **4** **7** **6** **5**

○ ○ ○ ○ ○ ○ ○

-01, 02
80 COL

○ ○ ○ ○ ○ ○ ○

DETAIL **A**
SCALE: NONE
JUMPER CONFIGURATIONS
(104)



DETAIL 13
SCALE: NONE
JUMPER CONFIGURATIONS
104

W X Y Z

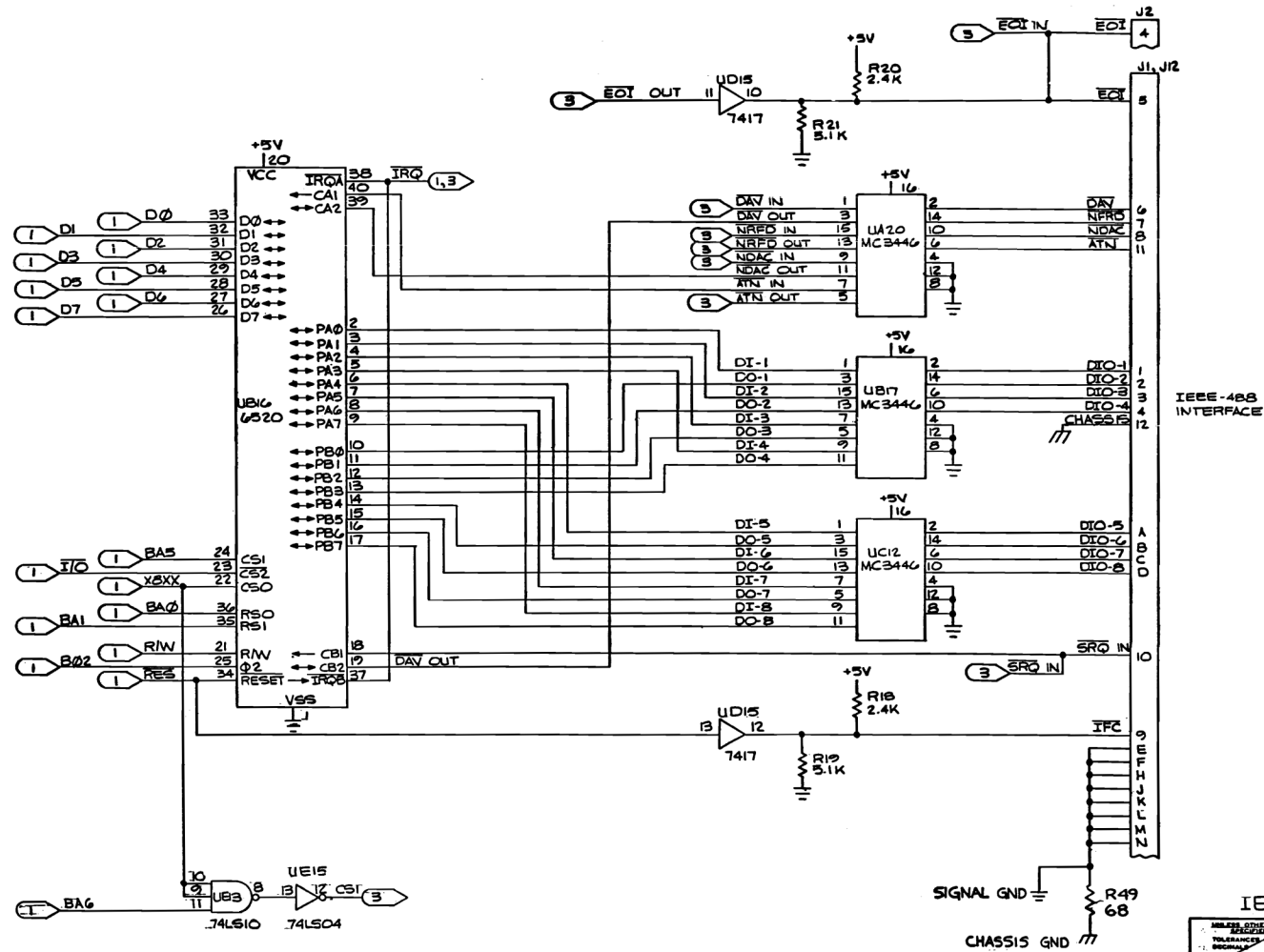
-01 THRU -04
32K

-05, -06
16K

DETAIL C
SCALE: NONE
JUMPER CONFIGURATIONS
104

UNLESS OTHERWISE SPECIFIED TOLERANCES ARE DECIMALS INCHES 1/16 1/8 3/16 1/4 3/8 1/2 5/8 3/4 7/8 1	DRAWN BY	DATE	commodore PCB ASSY- UNIV. DYNAMIC PET
	CHKD	1-11-77	
	ENGR		
	APPR		
MATERIAL	USED ON	HEAT ASBY	SIZE D 8032080 SCALE 1/1 SHEET 7 OF 7

REVISIONS			
LYR	ZONE	DESCRIPTION	DATE APPROVED
---	---	SEE SHEET 1	

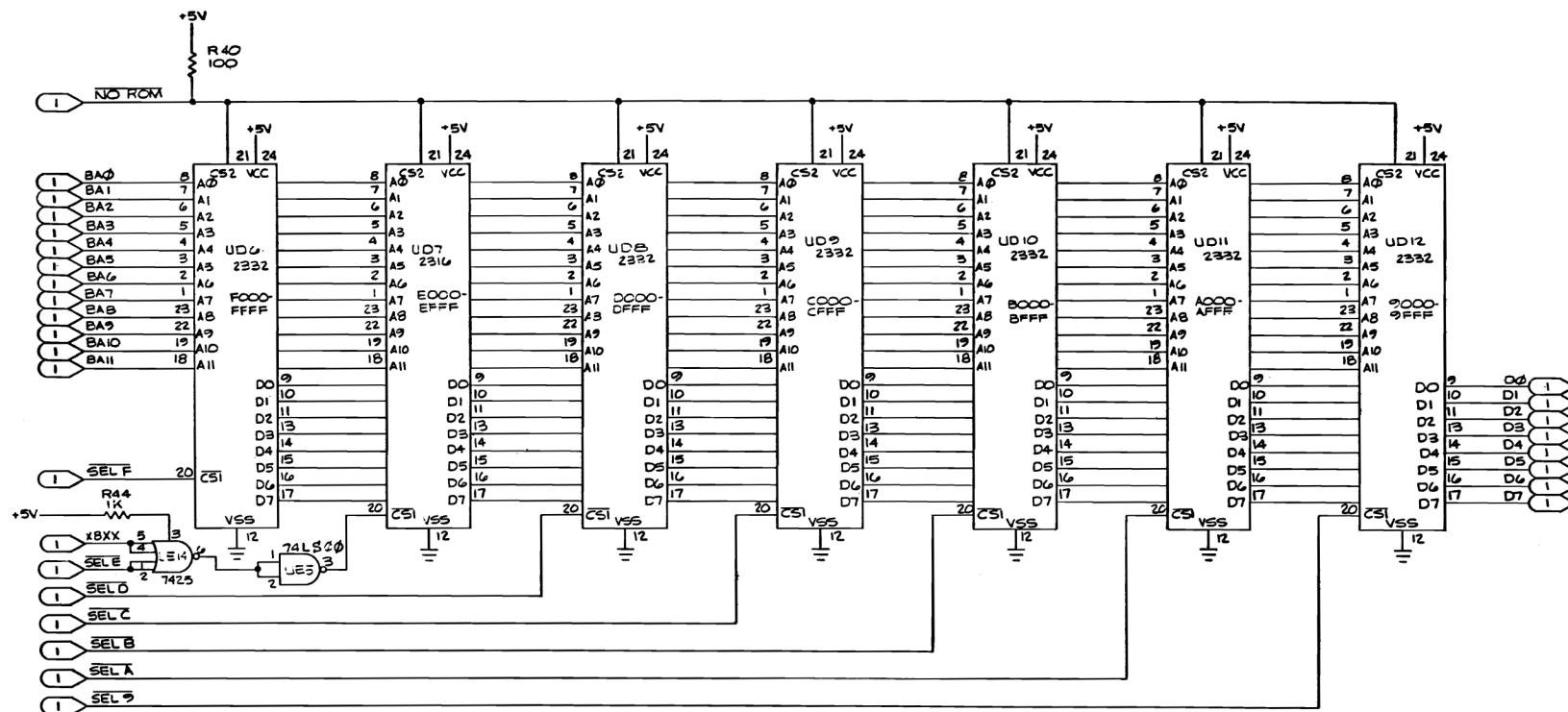


IEEE-488 INTERFACE

COMMODORE LOGIC DIAGRAM UNIVERSAL DYNAMIC PET		DRAWN BY: DATE: CHECKED: APPROVED: USED ON: NEXT ASS:
SIZE D 8032081	REV E	SCALE: NONE SHEET 2 OF 11



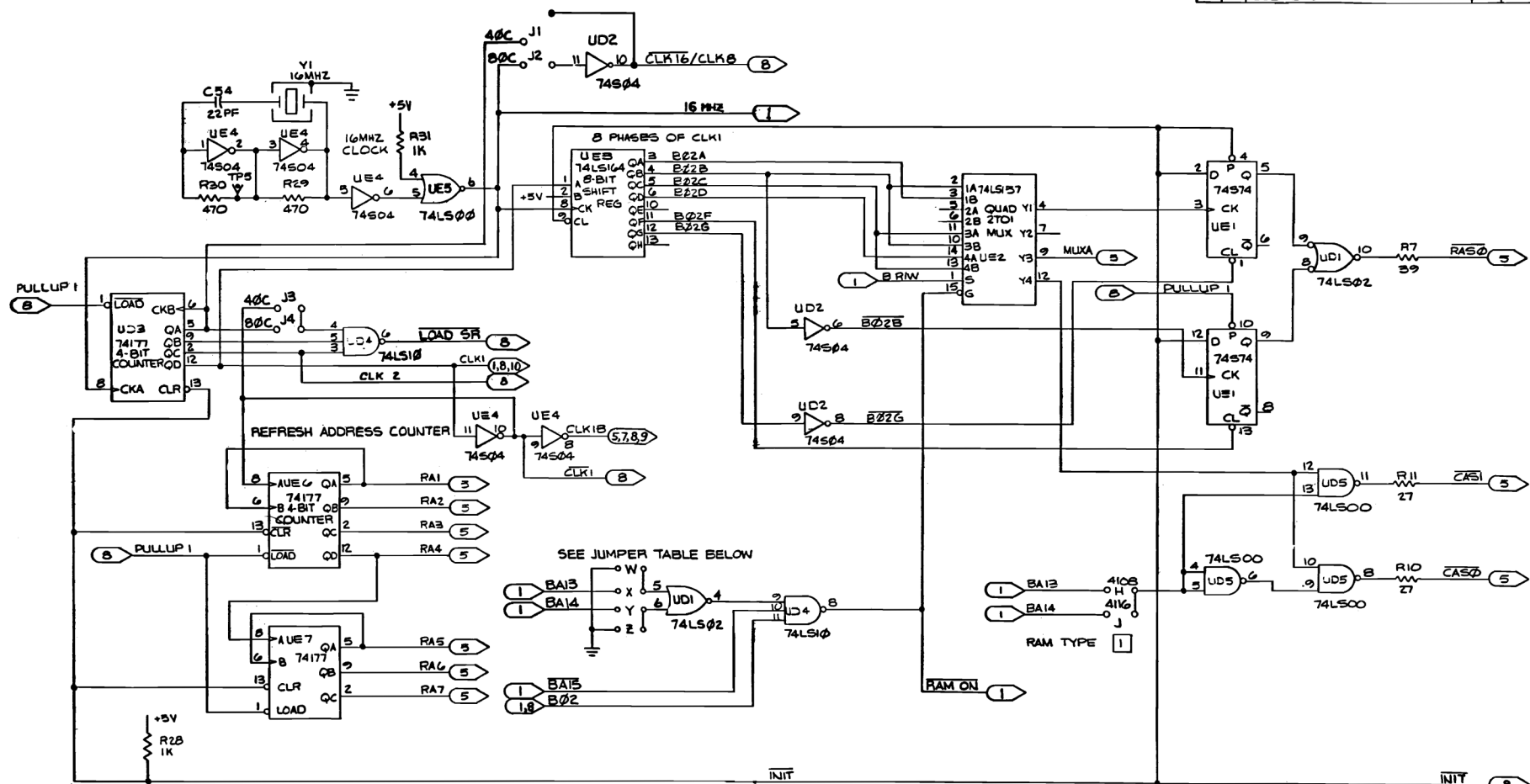
REVISIONS			
LTB	ZONE	DESCRIPTION	DATE APPROVED
		SEE SHEET 1	



ROMS

ADDRESS OTHERS TOLERANCE DECIMAL APPS MATERIAL PRODU	DRAWN BY DATE CHECK ENGR APPS USED ON NEXT ASSY	commodore LOGIC DIAGRAM UNIVERSAL DYNAMIC PET SIZE D 8032081 SCALE NONE SHEET 4 OF 11
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REVISIONS			
LTR	ZONE	DESCRIPTION	DATE APPROVED
SEE SHEET 1			



JUMPER TABLE

MEMORY JUMPERS			
	32K	16K	8K
W	○	○	○
X	○	○	○
Y	○	○	○
Z	○	○	○

MASTER TIMING

UNLESS OTHERWISE SPECIFIED	DESIGNED BY	DATE
TOLERANCES	CHECKED	
RESISTORS	DRIVEN	
CAPACITORS	LEVEL	
MATERIAL	USED ON	NEXT ASPT
PRINTED		

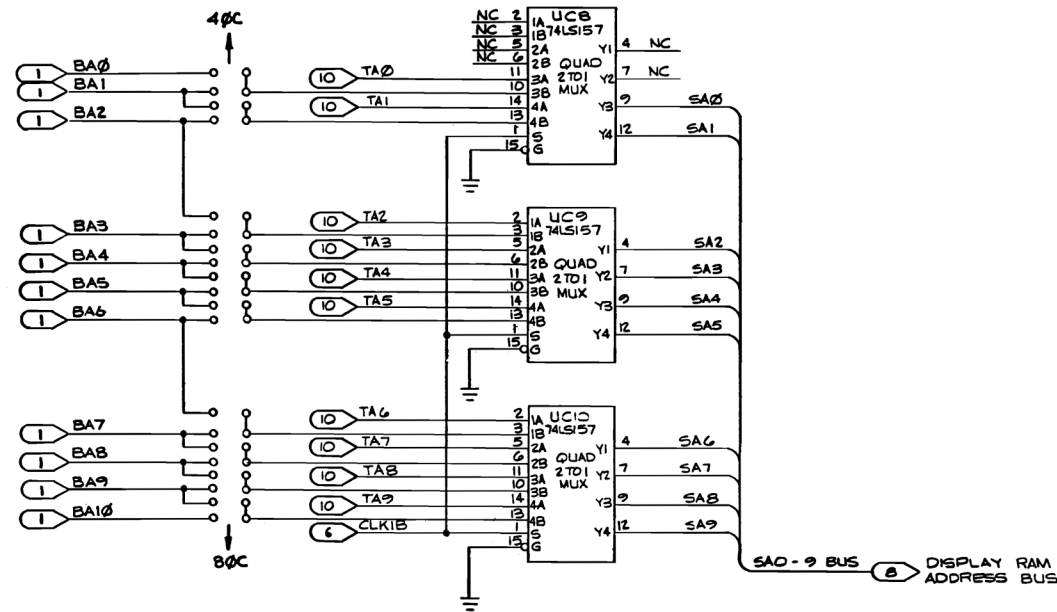
commodore

LOGIC DIAGRAM
UNIVERSAL
DYNAMIC PET

SIZE D 8032081 REV E

SCALE NONE SHEET 6 OF 11

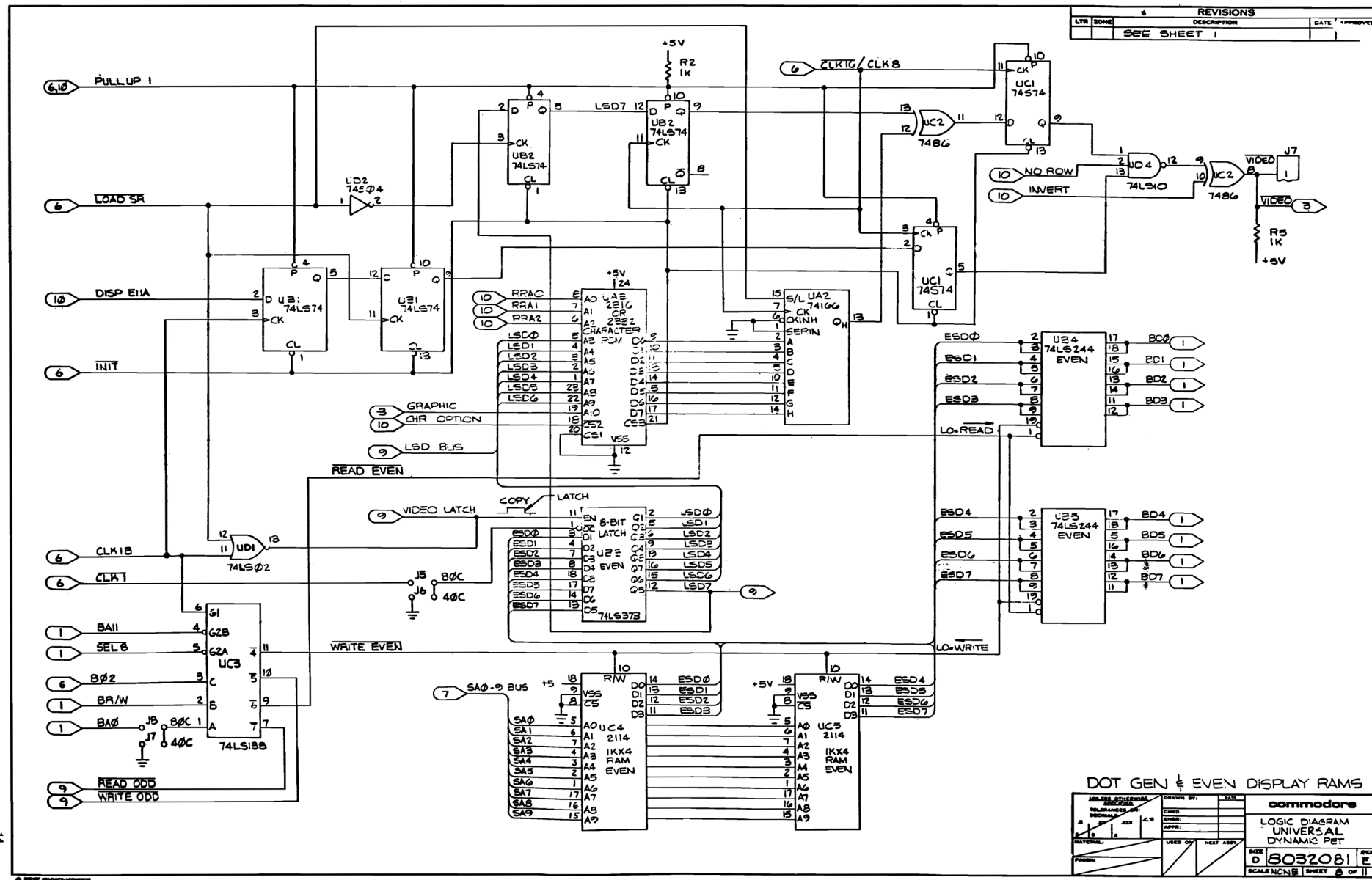
REVISIONS			
LTR	DATE	DESCRIPTION	APPROVED
		SEE SHEET 1	



DISPLAY ADDRESS MUX

ANALYST: TOLERANCES: DIMENSIONS: MATERIAL: FINISH:		DRAWN BY: CHECKED: USED ON: HEAT ASBY:	DATE: COMMODORE LOGIC DIAGRAM UNIVERSAL DYNAMIC PET SIZE: SCALE: NONE SHEET: 7 OF 11
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REVISIONS			DATE	APPROVED
LYR	ZONE	DESCRIPTION		
		SEE SHEET 1		



DOT GEN & EVEN DISPLAY RAMS

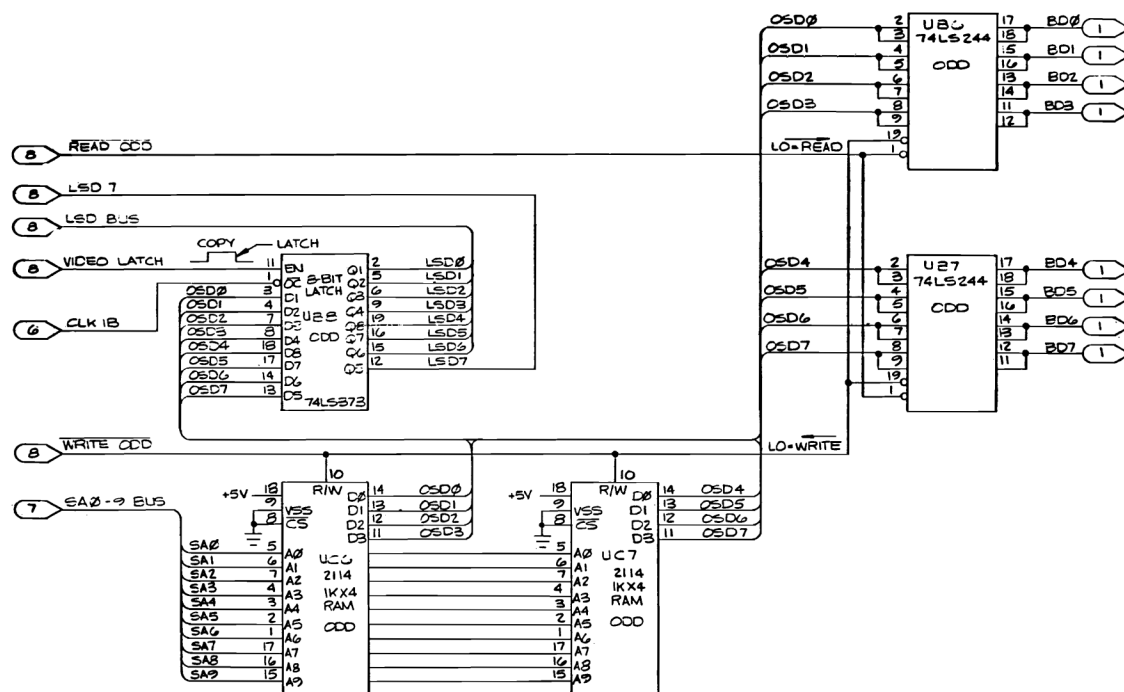
commodore

LOGIC DIAGRAM
UNIVERSAL
DYNAMIC PET

SIZE: 8032081 E

SCALE: NONE SHEET 8 OF 11

REVISIONS			
LYR	BOM#	DESCRIPTION	DATE
		SEE SHEET 1	



ODD DISPLAY RAMS

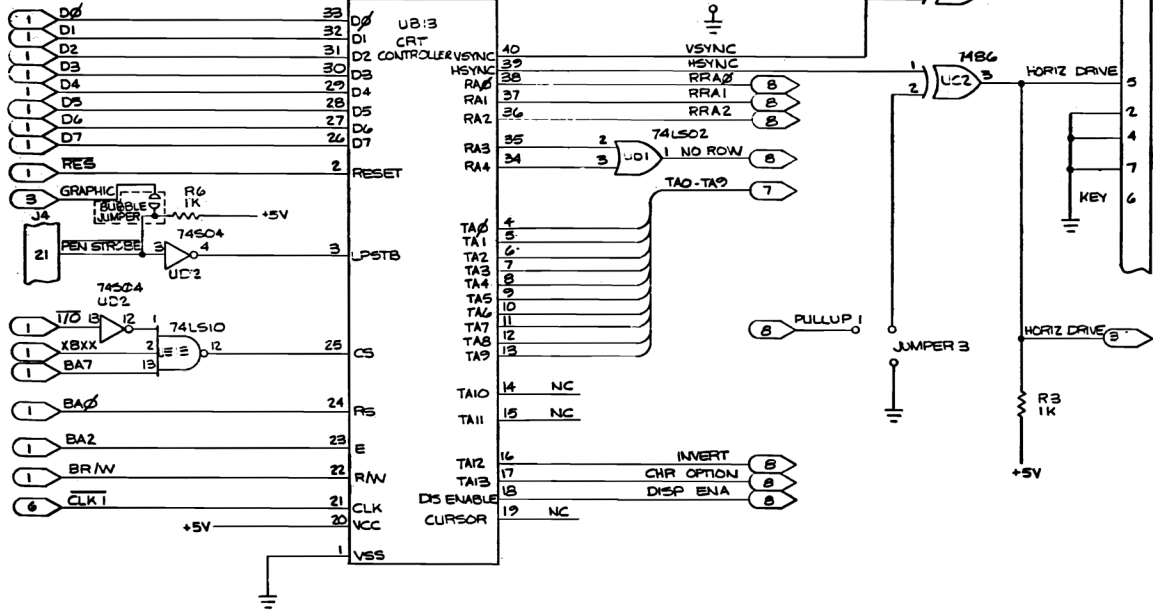
UNLESS OTHERWISE SPECIFIED		DRAWN BY: ETS	
TOLERANCES ARE DECIMALS		CHECKED: _____	
MATERIAL: _____		CIRCUIT: _____	
FINISH: _____		APPROVED: _____	
USED ON: _____		NEXT ASSY: _____	
SCALE: NONE		SHEET: 9 OF 11	

commodore

LOGIC DIAGRAM
UNIVERSAL
DYNAMIC PET

SIZE: D 8032081
REV: E

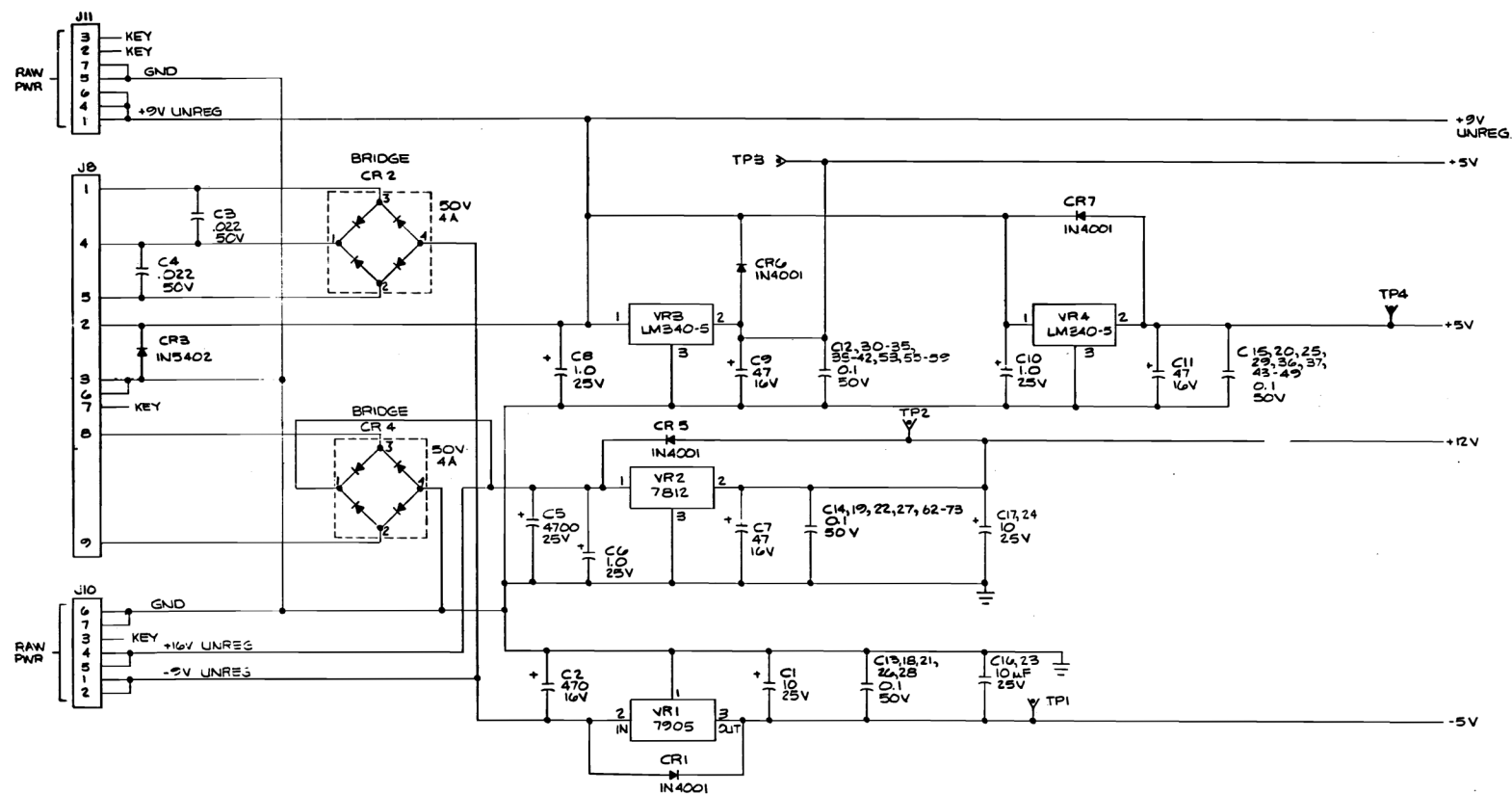
REVISIONS				
LTR	ZONE	DESCRIPTION	DATE	APPROVED
		SEE: SHEET 1		



CRTC AND SYNC BUFFERS

MILES STANDARD TOLANCES OR DECIMALS 1/2 3/4 1/4 1/8		DRAWN BY: _____ DATE _____ CHECK _____ TWEED _____ APPR: _____		commodore LOGIC DIAGRAM UNIVERSAL DYNAMIC PET	
MATERIAL: _____ FINISH: _____		USED ON: _____ NEXT ASSY: _____		SIZE _____ REV _____ D 8032081 E SCALE NONE SHEET 10 OF 11	

REVISIONS			
LTR	ZONE	DESCRIPTION	DATE APPROVED
SEE SHEET 1			



VOLTAGE REGULATOR

commodore LOGIC DIAGRAM UNIVERSAL DYNAMIC PET		SIZE D 8032081 SCALE NONE SHEET 11 OF 11
UNLESS OTHERWISE SPECIFIED TOLERANCES: RESISTORS: 1% CAPS: 10% DIODES: 1% TRANSISTORS: 1%	DRAWN BY: _____ CHECKED BY: _____ APPR: _____ USED ON: _____ NEXT ASSY: _____	REV E