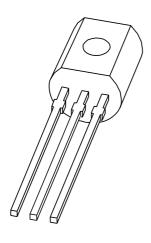
DISCRETE SEMICONDUCTORS

DATA SHEET



BC635; BC637; BC639 NPN medium power transistors

Product specification Supersedes data of 1999 Apr 23 2001 Oct 10





NPN medium power transistors

BC635; BC637; BC639

FEATURES

- High current (max. 1 A)
- Low voltage (max. 80 V).

APPLICATIONS

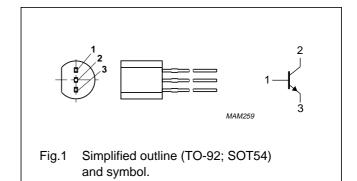
• Driver stages of audio/video amplifiers.

DESCRIPTION

NPN transistor in a TO-92; SOT54 plastic package. PNP complements: BC636, BC638 and BC640.

PINNING

PIN	DESCRIPTION
1	base
2	collector
3	emitter



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter			
	BC635		_	45	V
	BC637		_	60	V
	BC639		_	100	V
V _{CEO}	collector-emitter voltage	open base			
	BC635		_	45	V
	BC637		_	60	V
	BC639		_	80	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)		_	1	А
I _{CM}	peak collector current		_	1.5	Α
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	0.83	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

NPN medium power transistors

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	150	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

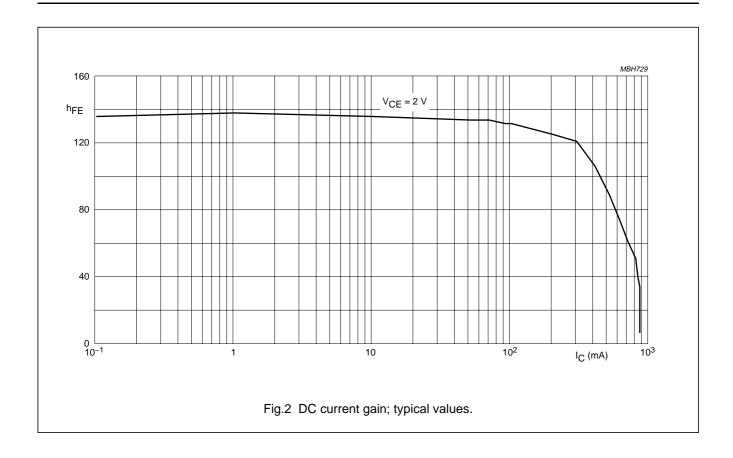
CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 30 V	_	100	nA
		I _E = 0; V _{CB} = 30 V; T _j = 150 °C	_	10	μΑ
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V	_	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; see Fig.2			
		$I_C = 5 \text{ mA}$	63	_	
		I _C = 150 mA	63	250	
		I _C = 500 mA	40	_	
	DC current gain	I _C = 150 mA; V _{CE} = 2 V; see Fig.2			
	BC639-10		63	160	
	BC635-16; BC637-16; BC639-16		100	250	
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA	_	500	mV
V _{BE}	base-emitter voltage	I _C = 500 mA; V _{CE} = 2 V	_	1	V
f _T	transition frequency	I _C = 50 mA; V _{CE} = 5 V; f = 100 MHz	100	_	MHz
h _{FE1} h _{FE2}	DC current gain ratio of the complementary pairs	I _C = 150 mA; V _{CE} = 2 V	_	1.6	

NPN medium power transistors

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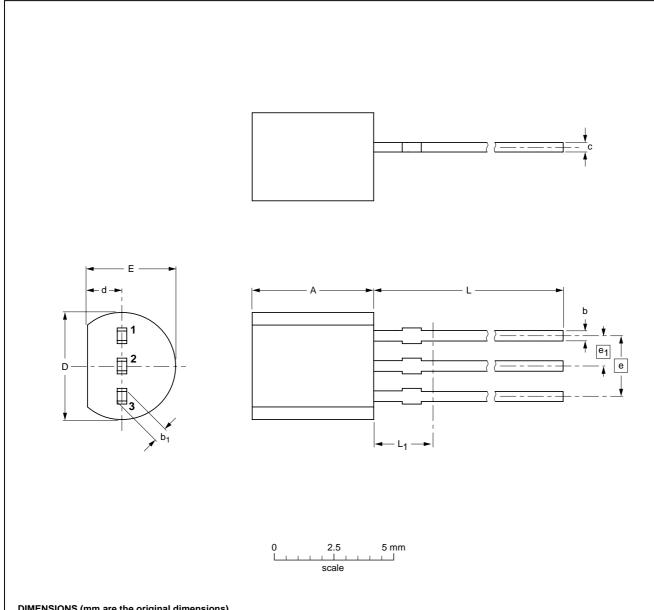
NPN medium power transistors

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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	Α	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE	REFERENCES			REFERENCES			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE	
SOT54		TO-92	SC-43			97-02-28	

2001 Oct 10 5

NPN medium power transistors

BC635; BC637; BC639

DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A.

Notes

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NPN medium power transistors

BC635; BC637; BC639

NOTES

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